

FORM PTO-1449		ATTY. DOCKET NO. 12-135.3US	SERIAL NO. 10/054,030
U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		APPLICANT Stephen E. Terry	
INFORMATION DISCLOSURE STATEMENT BY APPLICANT		FILING DATE January 22, 2002	GROUP 2662
(Use several sheets if necessary)			

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EXAMINER INITIAL		DOCUMENT NUMBER								DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
QU	AI	5	3	1	7	5	6	2		5/94	Nardin et al.			
/	AJ	5	8	0	2	3	1	0		9/98	Rajaraman			

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER								DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
														YES	NO
AK	W	O	9	6	0	89	35			3/96	PCT				
AL	W	O	9	8	4	72	53			10/98	PCT				X
AM	W	O	9	9	0	97	75			2/99	PCT				
AN	E	P	0	91	20	15	A2			4/99	Europe				
AO	E	P	0	97	73	93	A1			2/00	Europe				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

AP	Chi-Qing Yang and Alapati V. S. Reddy, "A Taxonomy for Congestion Control Algorithms in Packet Switching Networks", IEEE Network, July/August 1995, pp. 34-45
AA	TS 25.321 V2.0.1 MAC Protocol Specification, TSG-RAN Working Group 2 (Radio Layer 2 and Radio Layer 3), Berlin, Germany, May 25-28, 1999.
AB	Stephen E. Terry, FACH Scheduling, Prioritization and Queue Management, 3GPP TSG-RAN WG2 #4, Berlin, Germany, May 25-28, 1999.

EXAMINER

Charles E. Hill

DATE CONSIDERED

2/29/04

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		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
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AC	E	P	09	12	01	5	A2	4/99	Europe					
AD	E	P	09	12	01	5	A3	5/99	Europe					
AE	E	P	09	77	39	3	A1	2/00	Europe					
AF	W	O	96	08	9	3	5	3/96	PCT					
AG	W	O	99	09	7	7	5	2/99	PCT					
AH	W	O	98	47	2	5	3	10/98	PCT				X	

EXAMINER
Donald E. Mills

2/29/04

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Sheet _1_ of _1_

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	APPLICANT Stephen E. Terry	
	FILING DATE January 22, 2002	GROUP 2662

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EXAMINER INITIAL		DOCUMENT NUMBER							DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
DM	AL	4	6	3	0	2	6	1	12/1986	Irvine			
	AM	5	2	7	8	8	2	8	01/1994	Chao			
	AN	5	7	2	9	5	4	0	03/1998	Wegrzyn			
	AO	5	8	3	8	6	7	7	11/1998	Kozaki et al.			
	AP	6	0	9	1	7	2	5	07/2000	Cheriton et al.			
	AQ	6	1	0	5	0	7	0	08/2000	Gaylord			
	AR	6	1	2	2	2	5	3	09/2000	Jones			
	AS	6	4	2	4	6	2	4	07/2002	Galand et al.			
	AT	6	4	9	6	5	1	3	12/2002	Takamichi			

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FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
													YES	NO
DM	AU	4	3	0	5	7	0	A2	06/1991	EP				
I	AV	8	7	7	5	1	2	A2	11/1998	EP				

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

DM	AW	Chao et al., "Queue Management with Multiple Delay and Loss Priorities for ATM Switches," IEEE International Conference on Communications, 1994, ICC 94, SUPERCOMM. New Orleans, LA. May 1-5, 1994, vol. 2, pgs. 1184-1189.
I	AX	Liebeherr et al., "Priority Queue Schedulers with Approximate Sorting in Output-Buffered Switches," IEEE Journal on Selecte Areas in Communications, Charlottesville, VA. June 1999, vol. 17, iss. 6 pgs. 1127-1144

EXAMINER <i>Donald E. Mills</i>	DATE CONSIDERED 2/29/04
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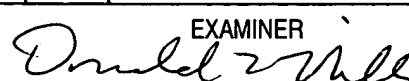
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DM	AC	E	P	09	12	01	5	A2	4/99	Europe				
DM	AD	E	P	09	12	01	5	A3	5/99	Europe				
DM	AE	E	P	09	77	39	3	A1	2/00	Europe				
DM	AF	W	O	96	08	9	3	5	3/96	PCT				
DM	AG	W	O	99	09	7	7	5	2/99	PCT				
DM	AH	W	O	98	47	2	5	3	10/98	PCT				

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